



High Breakdown Voltage (-201) β -Ga₂O₃ Schottky Rectifiers

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Abstract— β -Ga₂O₃ Schottky barrier diodes were fabricated in a vertical geometry structure consisting of Ni/Au rectifying contacts without edge termination on Si-doped epitaxial layers ($10\ \mu\text{m}$, $n\sim 4\times 10^{15}\ \text{cm}^{-3}$) on Sn-doped bulk Ga₂O₃ substrates with full-area Ti/Au back Ohmic contacts. The reverse breakdown voltage, V_{BR} , was a function of rectifying contact area, ranging from 1600 V at $3.1\times 10^{-6}\ \text{cm}^2$ ($20\text{-}\mu\text{m}$ diameter) to $\sim 250\ \text{V}$ at $2.2\times 10^{-3}\ \text{cm}^2$ (0.53-mm diameter). The current density near breakdown was not strongly dependent on contact circumference but did scale with contact area, indicating that the bulk current contribution was dominant. The lowest ON-state resistance, R_{ON} , was $1.6\ \text{m}\Omega\cdot\text{cm}^2$ for the largest diode and $25\ \text{m}\Omega\cdot\text{cm}^2$ for the 1600-V rectifier, leading to a Baliga figure-of-merit ($V_{\text{BR}}^2/R_{\text{ON}}$) for the latter of approximately $102.4\ \text{MW}\cdot\text{cm}^{-2}$. The ON-OFF ratio was measured at a forward voltage of 1.3 V and ranged from 3×10^7 to 2.5×10^6 for reverse biases from -5 to $-40\ \text{V}$ and showed only a small dependence on temperature in the range from $25\ ^\circ\text{C}$ to $100\ ^\circ\text{C}$.

Index Terms— Gallium Oxide, Schottky diode, rectifiers, reverse breakdown voltage.

I. INTRODUCTION

Ga₂O₃ has a theoretical Baliga figure of merit (defined as $V_{\text{BR}}^2/R_{\text{ON}}$, where V_{BR} is the reverse breakdown voltage and R_{ON} is the on-state resistance) significantly higher

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than more familiar wide bandgap semiconductors, due mainly to its larger bandgap ($\sim 4.5\text{--}4.8\ \text{eV}$) compared to that of 4H or 6H-SiC and GaN ($\sim 3.0\text{--}3.4\ \text{eV}$) [1]–[4]. It is worth noting that Omura *et al.* [5] recently measured a direct gap of $\sim 4.5\ \text{eV}$, smaller than the often-quoted $4.8\ \text{eV}$ common in the literature. The theoretical breakdown electric field is $\sim 8\ \text{MV/cm}$ [6]–[17], with experimental demonstrations as high as $3.8\ \text{MV/cm}$ [8] and this is already higher than the bulk critical field strengths of both GaN and SiC [18]–[20]. There are five different phases of Ga₂O₃, the most prominent being the α - and β -phases [3], [4]. The former has the same corundum crystal structure as Al₂O₃ or sapphire, leading to the possibility of high quality epitaxial layers of Ga₂O₃ on sapphire substrates [4]. Subsequent lift-off of these Ga₂O₃ layers and transfer to more thermally conducting substrates is a possible approach to developing cost-effective wide bandgap semiconductor power electronics. Large diameter, twin-free, β -phase bulk, insulating or conducting β -Ga₂O₃ crystals have been grown by edge-defined film-fed (EFG) and by Czochralski and float zone methods and are commercially available [3]. Ga₂O₃ is well-placed as a potential option for high power electronics for use in hybrid electric vehicles, power conditioning in large industrial motors and power distribution and switching applications operating at high temperatures or voltages and currents beyond the capabilities of Si [7], [9]. There have been a number of impressive demonstrations to date of the capability of power Ga₂O₃ Schottky diode rectifiers, metal-semiconductor field-effect transistors (MESFETs) and 750V field-plate terminated metal-oxide-semiconductor field-effect transistors (MOSFETs) [8]–[17].

Schottky rectifiers are attractive because of their fast switching speed, which is important for improving the efficiency of inductive motor controllers and power supplies, as well as their low on-state losses. Their switching speed not suffer from minority-carrier storage effects present in bipolar devices [18]–[20]. Compared with lateral diodes grown on insulating substrates, vertical geometry Schottky diodes on conducting substrates can deliver higher power with full back side Ohmic electrodes and have higher current capability since they take advantage of the entire conducting area. Edge termination can also enhance the performance by preventing premature breakdown due to field crowding around the contact periphery. Initial reports by Sasaki *et al.* [16] have shown vertical rectifiers with V_{BR} of $\sim 150\ \text{V}$ on n-type homoepitaxial β -Ga₂O₃ as well as on single-crystal substrates. Oh *et al.*

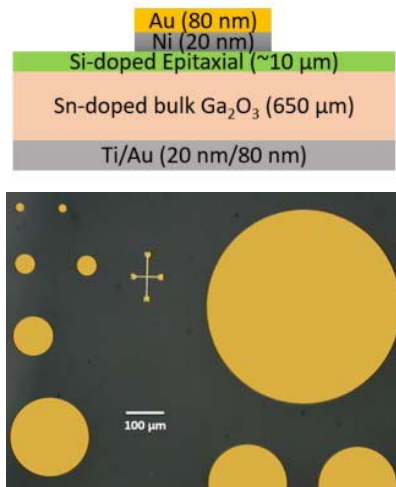


Fig. 1. Schematic of vertical Ni/Au Schottky diode on Ga₂O₃ epi layer on a conducting β -Ga₂O₃ substrate (top) and top-view microscope image of the fabricated β -Ga₂O₃ diodes of different diameter (bottom).

showed excellent performance of 210V Ni/ β -Ga₂O₃ vertical Schottky diodes up to 225 °C [20]. Konishi *et al.* [17] fabricated field-plated Ga₂O₃ Schottky barrier diodes on a Si-doped n^- -Ga₂O₃ drift layer grown by halide vapor phase epitaxy on a Sn-doped n^+ -Ga₂O₃ (001) substrate and achieved specific on-resistance of 5.1 m Ω ·cm² and a breakdown voltage of 1076V [17]. The diode diameter was 200-400 μ m.

In this letter we show that Schottky rectifiers without edge termination on epitaxial layers of β -Ga₂O₃ on bulk conducting substrates can achieve V_{BR} values up to 1600V and that the reverse currents are dominated by bulk current conduction. The diode on-off ratios are in the range $2.5 \times 10^6 - 3 \times 10^7$ for reverse biases from -5 to -40 V and showed only a small dependence on temperature in the range 25-100 °C.

II. EXPERIMENTAL

The diodes were fabricated on vertical structures consisting of epitaxial layers (10 μ m thick) of lightly Si-doped n -type Ga₂O₃ grown by Metal Organic Chemical Vapor Deposition on n^+ bulk, β -phase Sn-doped Ga₂O₃ single crystal wafers (~ 650 μ m thick) with (-201) surface orientation grown by the edge-defined film-fed method. These substrates had carrier concentration of 3.6×10^{18} cm⁻³ from Hall measurements. The dislocation density from etch pit observation was $\sim 10^3$ cm⁻².

Diodes were fabricated by depositing full area back Ohmic contacts of Ti/Au (20 nm/80 nm) by E-beam evaporation. We obtained Ohmic behavior without the need for rapid thermal annealing or implantation steps. The front sides were patterned by lift-off of E-beam deposited Schottky contacts Ni/Au (20 nm/80 nm) on the epitaxial layers. The diameter of these contacts ranged from 20 μ m to 0.53 mm. Fig. 1 shows a schematic of the rectifier structure (top) and optical images of some of the completed diodes with different diameters (bottom). Current-voltage (I-V) and capacitance-voltage (C-V) characteristics were recorded from 25–100 °C on an Agilent 4145B parameter analyzer using a heated probe station. Fig. 2 shows the C^{-2} -V plot to determine the carrier density in the epitaxial layer. The slope corresponds to a

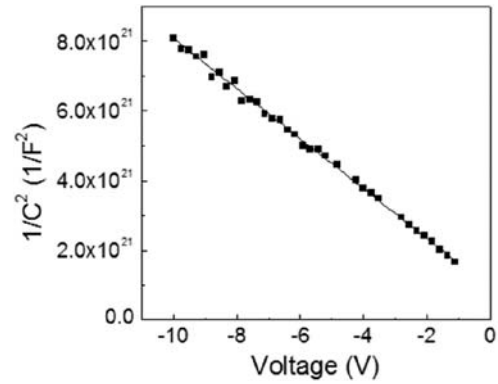


Fig. 2. Inverse depletion layer capacitance squared (C^{-2}) measured at 10 MHz versus reverse voltage (V) plot to determine the doping level in the epitaxial Ga₂O₃ layer. The extracted value was 4.02×10^{15} cm⁻³.

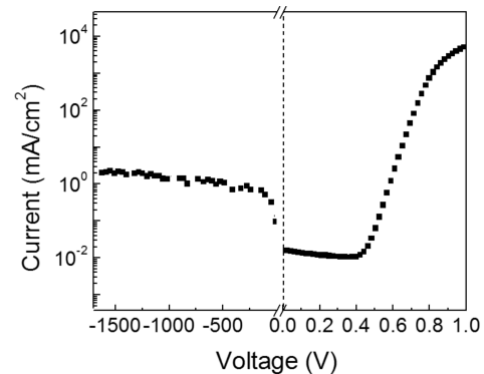


Fig. 3. Forward and reverse current density-voltage characteristics from a 20 μ m diameter diode.

donor density of 4.02×10^{15} cm⁻³. We also measured the Schottky barrier height and ideality factor from the linear portion of the forward I - V characteristics. At 25 °C, the barrier height was 1.22 eV with an ideality factor of 1.07, consistent with previous reports for Ni on Ga₂O₃. The data was consistent with thermionic emission being the dominant current transport mechanism [20]–[22]. The barrier height decreased with temperature, reaching a value of 1.00 eV at 100 °C with an ideality factor of 1.33.

III. RESULTS AND DISCUSSION

Fig. 3 shows the forward and reverse current density-voltage (J - V) characteristic from a 20 μ m diameter diode. The V_{BR} at room temperature was ~ 1600 V for this diameter smaller diode and 250V for the largest diameter. This trend is typical of newer materials technologies still being optimized in terms of defect density [23]. The on-resistance (R_{on}) values was approximately 25 m Ω ·cm² for the smallest diodes and 1.6 m Ω ·cm² for the largest diameter devices. The Baliga figure-of-merit [23] (V_{BR}^2/R_{on}) for the former was approximately 102.4 MW·cm⁻², more than an order of magnitude larger than previous reports [20]. The breakdown field was 1.6 MV·cm⁻¹, which is still well below the theoretical value discussed earlier [8]. We did not use any edge termination methods to reduce electrical field crowding at the contact edges, which is where breakdown is likely occurring. Our results therefore represent a minimum breakdown field

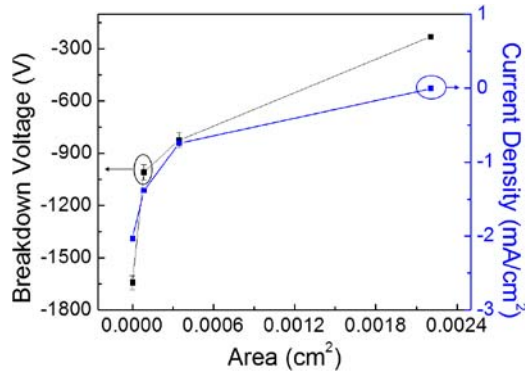


Fig. 4. Breakdown voltage and current density as a function of Schottky contact area.

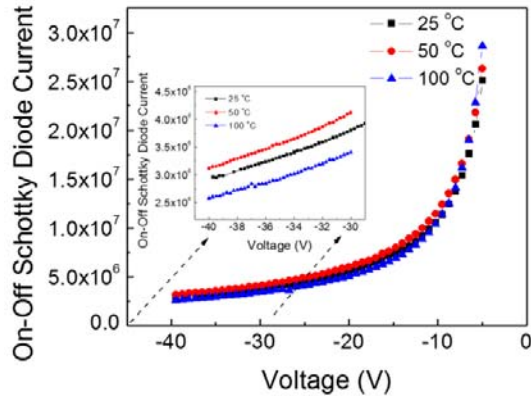


Fig. 5. Diode on/off ratios for temperatures in the range 25-100 °C as a function of reverse bias. The forward bias was held constant at 1.3V.

strength of currently available state-of-the-art material and use of field-plate or guard-ring structures as edge termination would enhance this further [17], [20], [23].

Fig. 4 shows the reverse breakdown voltage and current density near breakdown as a function of diode contact area. The current density near breakdown was not strongly dependent on contact circumference but scaled with contact area, indicating the bulk current contribution was dominant [23]. This indicates that the Ga_2O_3 surface is stable enough during device processing steps that it does not dominate the device performance. The V_{BR} values were a function of rectifying contact area, ranging from 1600V at $3.1 \times 10^{-6} \text{ cm}^2$ (20 μm diameter) to $\sim 250\text{V}$ at $2 \times 10^{-3} \text{ cm}^2$ (0.53 mm diameter). The decrease with increasing diode area is expected since the larger diodes have a higher probability of including a defect with potential for initiating breakdown [23].

Fig. 5 Shows the on-off current ratio measured at a fixed forward voltage of 1.3V and reverse biases from -5 to -40 V. The on-of ratios ranged from 3×10^7 to 2.5×10^6 for this range of biases and showed only a small dependence on temperature in the range 25-100 °C. This is promising for device operating temperatures in this range, since there would be little change in performance characteristics.

IV. SUMMARY AND CONCLUSIONS

Vertical geometry $\beta\text{-Ga}_2\text{O}_3$ Schottky rectifiers were fabricated on epilayers on bulk substrates showed V_{BR} values up to 1600 V for 20 μm diameter contacts and $\sim 250\text{V}$ for rectifiers with 0.53 mm diameter. The figure-of-merit was

102.4 MWcm^{-2} for the smaller sizes. These results show the rapid progress in developing high quality $\beta\text{-Ga}_2\text{O}_3$ epi and bulk growth and that Schottky rectifiers in this materials system exhibit impressive power switching applications.

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REFERENCES

- [1] S. Fujita, "Wide-bandgap semiconductor materials: For their full bloom," *Jpn. J. Appl. Phys.*, vol. 54, no. 3, p. 030101, 2015, doi: 10.7567/JJAP.54.030101.
- [2] T. P. Chow, I. Omura, M. Higashiwaki, H. Kawarada, and V. Pala, "Smart power devices and ICs using GaAs and wide and extreme bandgap semiconductors," *IEEE Trans. Electron Devices*, vol. 64, no. 3, pp. 856–873, Mar. 2017, doi: 10.1109/TED.2017.2653759.
- [3] A. Kuramata, K. Koshi, S. Watanabe, Y. Yamaoka, T. Masui, and S. Yamakoshi, "High-quality $\beta\text{-Ga}_2\text{O}_3$ single crystals grown by edge-defined film-fed growth," *Jpn. J. Appl. Phys.*, vol. 55, no. 12, p. 1202A2, Nov. 2016, doi: 10.7567/JJAP.55.1202A2.
- [4] S. I. Stepanov, V. I. Nikolaev, V. E. Bougrov, and A. E. Romanov, "Gallium oxide: Properties and applications—A review," *Rev. Adv. Mater. Sci.*, vol. 44, no. 63, pp. 63–86, Apr. 2016. [Online]. Available: http://www.ipme.ru/e-journals/RAMS/no_14416/06_14416_stepanov.pdf
- [5] T. Onuma, S. Saito, K. Sasaki, T. Masui, T. Yamaguchi, T. Honda, and M. Higashiwaki, "Valence band ordering in $\beta\text{-Ga}_2\text{O}_3$ studied by polarized transmittance and reflectance spectroscopy," *Jpn. J. Appl. Phys.*, vol. 54, no. 11, p. 112601, Oct. 2015, doi: 10.7567/JJAP.54.112601.
- [6] M. Higashiwaki, K. Sasaki, A. Kuramata, T. Masui, and S. Yamakoshi, "Gallium oxide (Ga_2O_3) metal-semiconductor field-effect transistors on single-crystal $\beta\text{-Ga}_2\text{O}_3$ (010) substrates," *Appl. Phys. Lett.*, vol. 100, no. 1, p. 013504, Jan. 2012, doi: 10.1063/1.3674287.
- [7] M. Higashiwaki, K. Sasaki, H. Murakami, Y. Kumagai, A. Koukitu, A. Kuramata, T. Masui, and S. Yamakoshi, "Recent progress in Ga_2O_3 power devices," *Semicond. Sci. Technol.*, vol. 31, no. 3, p. 034001, Jan. 2016, doi: 10.1088/0268-1242/31/3/034001.
- [8] A. J. Green, K. D. Chabak, E. R. Heller, R. C. Fitch, M. Baldini, A. Fiedler, K. Irmscher, G. Wagner, Z. Galazka, S. E. Tetlak, A. Crespo, K. Leedy, and G. H. Jessen, "3.8 MV/cm breakdown strength of MOVPE-grown Sn-doped $\beta\text{-Ga}_2\text{O}_3$ MOSFETs," *IEEE Electron Device Lett.*, vol. 37, no. 7, pp. 902–905, Jul. 2016, doi: 10.1109/LED.2016.2568139.
- [9] M. Higashiwaki, K. Sasaki, A. Kuramata, T. Masui, and S. Yamakoshi, "Development of Gallium oxide power devices," *Phys. Status Solidi A*, vol. 211, no. 1, pp. 21–26, Jan. 2014, doi: 10.1002/psa.201470201.
- [10] M. H. Wong, K. Sasaki, A. Kuramata, S. Yamakoshi, and M. Higashiwaki, "Field-plated Ga_2O_3 MOSFETs with a breakdown voltage of over 750 V," *IEEE Electron Device Lett.*, vol. 37, no. 2, pp. 212–215, Feb. 2016, doi: 10.1109/LED.2015.2512279.
- [11] M. J. Tadjerz, N. A. Mahadik, V. D. Wheeler, E. R. Glaser, L. Ruppalt, A. D. Koehler, K. D. Hobart, C. R. Eddy, Jr., and F. J. Kub, "Editors' choice communication—A (001) $\beta\text{-Ga}_2\text{O}_3$ MOSFET with +2.9 V threshold voltage and HfO_2 gate dielectric," *ECS J. Solid State Sci. Technol.*, vol. 5, no. 9, pp. 468–470, Jul. 2016, doi: 10.1149/2.0061609jss.
- [12] K. D. Chabak, N. Moser, A. J. Green, D. E. Walker, Jr., S. E. Tetlak, E. Heller, A. Crespo, R. Fitch, J. P. McCandless, K. Leedy, M. Baldini, G. Wagner, Z. Galazka, X. Li, and G. Jessen, "Enhancement-mode Ga_2O_3 wrap-gate fin field-effect transistors on native (100) $\beta\text{-Ga}_2\text{O}_3$ substrate with high breakdown voltage," *Appl. Phys. Lett.*, vol. 109, no. 21, p. 213501, Nov. 2016, doi: 10.1063/1.4967931.
- [13] M. Higashiwaki, K. Sasaki, T. Kamimura, M. H. Wong, D. Krishnamurthy, A. Kuramata, T. Masui, and S. Yamakoshi, "Depletion-mode Ga_2O_3 metal-oxide-semiconductor field-effect transistors on $\beta\text{-Ga}_2\text{O}_3$ (010) substrates and temperature dependence of their device characteristics," *Appl. Phys. Lett.*, vol. 103, no. 12, p. 123511, Sep. 2013, doi: 10.1063/1.4821858.

- [14] M. Higashiwaki, K. Konishi, K. Sasaki, K. Goto, K. Nomura, Q. T. Thieu, R. Togashi, H. Murakami, Y. Kumagai, B. Monemar, A. Koukitsu, A. Kuramata, and S. Yamakoshi, "Temperature-dependent capacitance-voltage and current-voltage characteristics of Pt/Ga₂O₃ (001) Schottky barrier diodes fabricated on n-Ga₂O₃ drift layers grown by halide vapor phase epitaxy," *Appl. Phys. Lett.*, vol. 108, no. 13, p. 133503, Mar. 2016, doi: 10.1063/1.4945267.
- [15] W. S. Hwang, A. Verma, H. Peelaers, V. Protasenko, S. Ruvimov, H. Xing, A. Seabaugh, W. Haensch, C. Van de Walle, Z. Galazka, M. Albrecht, R. Fornari, and D. Jena, "High-voltage field effect transistors with wide-bandgap β -Ga₂O₃ nanomembranes," *Appl. Phys. Lett.*, vol. 104, no. 20, p. 203111, May 2014, doi: 10.1063/1.4879800.
- [16] K. Sasaki, M. Higashiwaki, A. Kuramata, T. Masui, and S. Yamakoshi, "Ga₂O₃ Schottky barrier diodes fabricated by using single-crystal β -Ga₂O₃ (010) substrates," *IEEE Electron Device Lett.*, vol. 34, no. 4, pp. 493–495, Mar. 2013, doi: 10.1109/LED.2013.2244057.
- [17] K. Konishi, K. Goto, H. Murakami, Y. Kumagai, A. Kuramata, S. Yamakoshi, and M. Higashiwaki, "1-kV vertical Ga₂O₃ field-plated Schottky barrier diodes," *Appl. Phys. Lett.*, vol. 110, no. 10, p. 103506, Mar. 2017, doi: 10.1063/1.4977857.
- [18] J. Millan, P. Godignon, X. Perpina, A. Perez-Tomas, and J. Rebollo, "A survey of wide bandgap power semiconductor devices," *IEEE Trans. Power Electron.*, vol. 29, no. 5, pp. 2155–2163, May 2014.
- [19] E. Bahat-Treidel, O. Hilt, R. Zhytnytska, A. Wentzel, C. Meliani, J. Würfl, and G. Tränkle, "Fast-switching GaN-based lateral power Schottky barrier diodes with low onset voltage and strong reverse blocking," *IEEE Electron Device Lett.*, vol. 33, no. 3, pp. 357–359, Mar. 2012, doi: 10.1109/LED.2011.2179281.
- [20] S. Oh, G. Yang, and J. Kim, "Electrical characteristics of vertical Ni/ β -Ga₂O₃ Schottky barrier diodes at high temperatures," *ECS J. Solid State Sci.*, vol. 6, no. 2, pp. 3022–3025, Sep. 2017, doi: 10.1149/2.0041702jss.
- [21] M. Mohamed, K. Irmscher, C. Janowitz, Z. Galazka, R. Manzke, and R. Fornari, "Schottky barrier height of Au on the transparent semiconducting oxide β -Ga₂O₃," *Appl. Phys. Lett.*, vol. 101, no. 13, p. 132106, Sep. 2012, doi: 10.1063/1.4755770.
- [22] M. J. Tadjer, V. D. Wheeler, D. I. Shahin, C. R. Eddy, and F. J. Kub, "Thermionic emission analysis of TiN and Pt Schottky contacts to β -Ga₂O₃," *ECS J. Solid State Sci. Technol.*, vol. 6, no. 4, pp. 165–168, Feb. 2017, doi: 10.1149/2.0291704jss.
- [23] B. J. Baliga, "Power semiconductor device figure of merit for high-frequency applications," *IEEE Electron Device Lett.*, vol. 10, no. 10, pp. 455–457, Oct. 1989, doi: 10.1109/55.43098.